

digital

8/A-200

8/A-400

The Price Leaders



Low-Cost, Proven Performance For The OEM

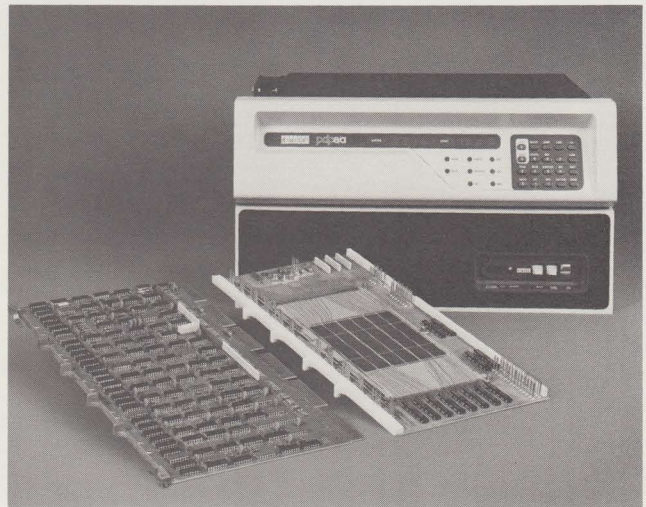
The 8/A family of miniprocessors has been expanded by the addition of the 8/A-200 and the 8/A-400 to provide proven performance at new low prices.

The 8/A family offers a wide variety of memory configurations as well as a full range of internal options and peripherals. The 8/A series can be configured with a complete range of memories, from 1K to 32K, including: CORE, read-only memory (ROM), random access memory (RAM), RAM/ROM combinations, and reprogrammable ROM (PROM). This flexibility enables DIGITAL to provide specific solutions to OEM computing needs.

- The original 8/A-100, with its ROM/RAM memory combinations, provides a cost-effective, superior performance alternative to hardwired logic controllers. It offers complete program integrity for dedicated applications which require small memory configurations. A new hardware technique enables the user to program ROM/RAM combinations exactly like CORE memory.
- The new 8/A-200 provides small memory users with an extremely low-cost system by utilizing the 4K MOS (RAM) chip. This system is optimized for 4K to 8K of memory.
- The new 8/A-400 provides the OEM user with all the capabilities of proven core memory plus a wide range of peripherals and software at low cost. It utilizes both 8K and 16K stacks and is designed to be cost-effective at memory configurations of 8K to 32K.

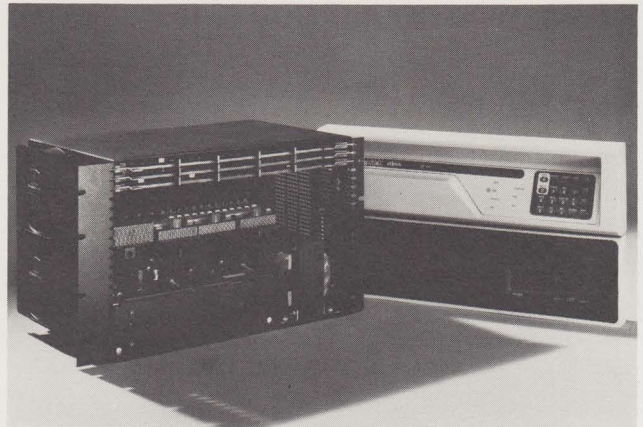
Features

- Low price for cost-conscious applications.
- Choice of memories: CORE, RAM, ROM, and PROM, allowing the user to choose the exact configuration required.
- Battery back-up standard with 8/A-100 and 8/A-200.
- PDP-8 software compatibility lets customers take advantage of PDP-8 operating systems like OS/8 and RTS/8 as well as ten years of software development.
- OMNIBUS compatibility lets existing PDP-8 options and peripherals be interfaced to the 8/A family.
- Reliability—CPU and memory are contained on single modules utilizing proven technology.
- Programmer's panel contains octal readout and keypad entry to improve operator efficiency.
- System modularity permits extremely easy access to all subassemblies for integration and maintenance.



Technical Summary

Word length:	12 bits
Cycle time:	1.5 microseconds
Hardware registers:	2
Auto-index registers:	8
Memory:	CORE 8K, 16K RAM 1K, 2K, 4K ROM 1K, 2K, 4K PROM 1K, 2K
Memory expansion:	Up to 32K
Battery back-up:	Standard on 8/A-100 and 8/A-200
Instruction set:	Same powerful instructions as PDP/8E
Options:	2 new option configurations available on single boards. 1. Front panel control Serial line unit Parallel I/O Real-time clock 2. Powerfail/auto-restart Memory extension Time-share control Bootstrap loader Plus... more than 75 options and peripherals available
Physical size:	26.67 cm. high by 48.26 cm. wide by 26.03 cm. deep (10½" by 19" by 10")





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